~~a memory;~~

an input for receiving request packets from a second video device indicating the second video device is capable of receiving data, wherein a request packet includes a stream identifier indicating a source for reading data from the memory in the video processing device; and

a1

an output for sending, in response to a request packet, data packets from the source to the second video device when data is available from the source, wherein a data packet includes a stream identifier for the second video device and a boundary signal portion including a boundary signal indicating that the data packet ends with a last component of the read data.

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7. (Amended) The video processing device of claim 5, wherein a request packet includes a request signal portion including a request signal from the second video device indicating a request for transfer of an amount of valid data.

a2

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17. (New) The video processing device of claim 5, wherein the input receives and the output sends data over a serial bus.

a3

18. (New) The video processing device of claim 17, wherein the serial bus is compliant with the IEEE-1394 standard.

REMARKS

In reply to the Office Action of May 10, 2002, in view of the foregoing amendments and following remarks, reconsideration is requested. Unelected claims 1-4 and 9-16 have been cancelled. Applicant hereby affirms the election of claims 5-8. Claims 5 and 7 were amended. Claim 6 was cancelled. A marked up version of these claims is attached. Dependent claims 17 and 18 have been added. Accordingly, Claims 5, 7, 8, 17 and 18 remain in the application of which claim 5 is independent.

Rejection under 35 U.S.C. 112, second paragraph

The Examiner stated that it is unclear whether "another video processing device" is different from or the same as "the other video processing device." The claims have been amended to avoid this ambiguity.